Course: Computer Architecture Semester: 1st term 2025/2026.	Lecturers: Dr. ……. Assistant: Eng ……., Eng…….,
Tutorial 3: Computer Architecture	

Q1. Chapter 1 Review Questions:

1. What was the fundamental design approach of the IAS computer?

A. Stored program concept
B. Vacuum tube technology
C. Binary logic
D. Integrated circuits

2. Who is attributed with the stored program concept?

A. Alan Turing
B. John von Neumann
C. Charles Babbage
D. Bill Gates

3. What is the function of the Instruction Register (IR) in the IAS machine?

A. To hold the address of the next instruction
B. To temporarily hold the opcode instruction being executed
C. To store the result of ALU operations
D. To specify the address in memory

4. What does the Memory Address Register (MAR) specify?

A. The address in memory of the word to be written from or read into the MBR
B. The opcode instruction being executed
C. The next instruction pair to be fetched from memory
D. The operands and results of ALU operations

5. What is the purpose of the Accumulator (AC)?

A. To hold the address of the next instruction
B. To store a word to be sent to the I/O unit
C. To temporarily hold operands and results of ALU operations
D. To specify the address in memory

Q2. Draw the IAS main structure

Q3. Mention the six registers and their functions