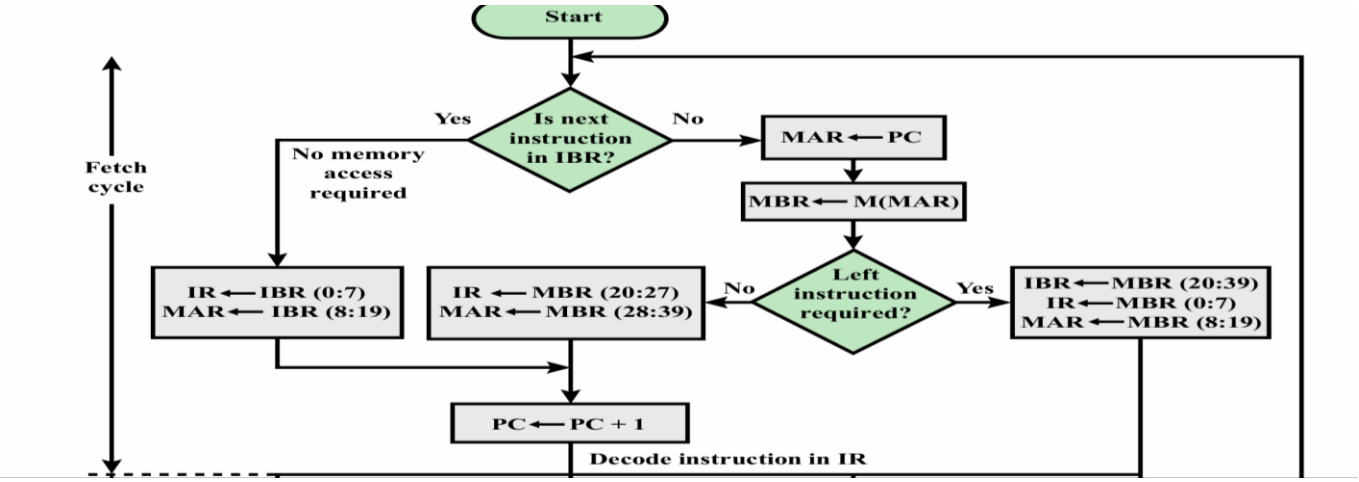
Course: Computer Architecture Semester: 1st term 2025/2026.	Lecturers: Dr. ……. Assistant: Eng ……., Eng…….,
Tutorial 4: Computer Architecture	

Q1. Draw the IAS memory format

Q2. Draw the IAS structure by its registers

Q3. Explain each block in the flow chart that is responsible for the fetch cycle



Q4. Illustrate the three CPU's basic operations and explain each one

Q5. Illustrate 4 basic categories for the instruction set classification